



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#7/B  
6/25/01  
Narrison

Application Serial No. .... 09/388,857  
Filing Date ..... September 1, 1999  
Inventor ..... Luan C. Tran  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... L. Schillinger  
Attorney's Docket No. .... MI22-878  
Title: Semiconductor Processing Methods Of Forming Transistors,  
Semiconductor Processing Methods Of Forming Dynamic Random  
Access Memory Circuitry, And Related Integrated Circuitry

**RESPONSE TO MAY 9, 2001 OFFICE ACTION**

To: Box Fee Amendment  
Assistant Commissioner for Patents  
Washington, D.C. 20231

From: Frederick M. Fliegel, Ph.D.  
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Responsive to the Office Action dated May 9, 2001, Applicant amends  
and remarks as follows:

**AMENDMENTS**